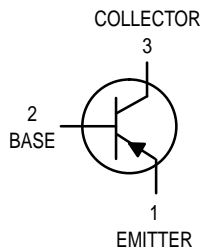
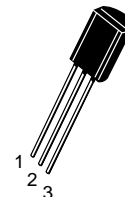


# One Watt Amplifier Transistor

## PNP Silicon



**MPS6726**  
**MPS6727**



CASE 29-05, STYLE 1  
TO-92 (TO-226AE)

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage MPS6726 MPS6727	$V_{CEO}$	–30 –40	Vdc
Collector–Base Voltage MPS6726 MPS6727	$V_{CBO}$	–40 –50	Vdc
Emitter–Base Voltage	$V_{EBO}$	–5.0	Vdc
Collector Current — Continuous	$I_C$	–1.0	Adc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.0 8.0	Watts mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	2.5 20	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	–55 to +150	$^\circ\text{C}$

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	125	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	50	$^\circ\text{C}/\text{W}$

### ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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### OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ( $I_C = -10 \text{ mAdc}, I_B = 0$ )	MPS6726 MPS6727	$V_{(BR)CEO}$	–30 –40	— —	Vdc
Collector–Base Breakdown Voltage ( $I_C = -100 \mu\text{Adc}, I_E = 0$ )	MPS6726 MPS6727	$V_{(BR)CBO}$	–40 –50	— —	Vdc
Emitter–Base Breakdown Voltage ( $I_E = -100 \mu\text{Adc}, I_C = 0$ )		$V_{(BR)EBO}$	–5.0	—	Vdc
Collector Cutoff Current ( $V_{CB} = -40 \text{ Vdc}, I_E = 0$ ) ( $V_{CB} = -50 \text{ Vdc}, I_E = 0$ )	MPS6726 MPS6727	$I_{CBO}$	— —	–0.1 –0.1	$\mu\text{Adc}$
Emitter Cutoff Current ( $V_{EB} = -5.0 \text{ Vdc}, I_C = 0$ )		$I_{EBO}$	—	–0.1	$\mu\text{Adc}$

# MPS6726 MPS6727

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
<b>ON CHARACTERISTICS(1)</b>				
DC Current Gain (I <sub>C</sub> = -100 mA <sub>dc</sub> , V <sub>CE</sub> = -1.0 V <sub>dc</sub> ) (I <sub>C</sub> = -1000 mA <sub>dc</sub> , V <sub>CE</sub> = -1.0 V <sub>dc</sub> )	h <sub>FE</sub>	60 50	— 250	—
Collector–Emitter Saturation Voltage (I <sub>C</sub> = -1000 mA <sub>dc</sub> , I <sub>B</sub> = -100 mA <sub>dc</sub> )	V <sub>CE(sat)</sub>	—	-0.5	V <sub>dc</sub>
Base–Emitter On Voltage (I <sub>C</sub> = -1000 mA <sub>dc</sub> , V <sub>CE</sub> = -1.0 V <sub>dc</sub> )	V <sub>BE(on)</sub>	—	-1.2	V <sub>dc</sub>
<b>SMALL–SIGNAL CHARACTERISTICS</b>				
Collector–Base Capacitance (V <sub>CB</sub> = -10 V <sub>dc</sub> , I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>cb</sub>	—	30	pF
Small–Signal Current Gain (I <sub>C</sub> = -50 mA <sub>dc</sub> , V <sub>CE</sub> = -10 V <sub>dc</sub> , f = 20 MHz)	h <sub>fe</sub>	2.5	25	—

1. Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 2.0%.

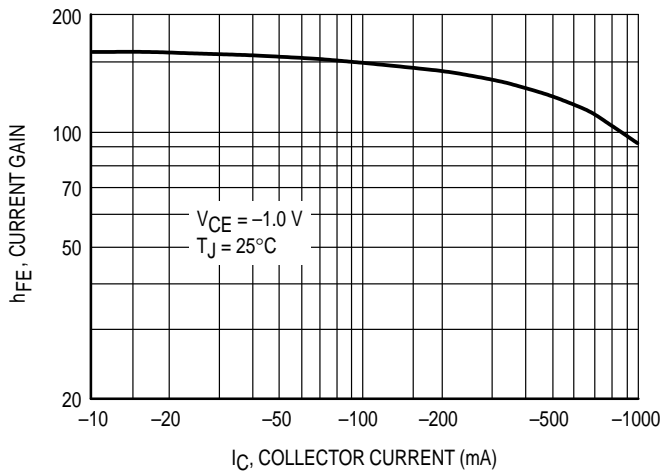


Figure 1. DC Current Gain

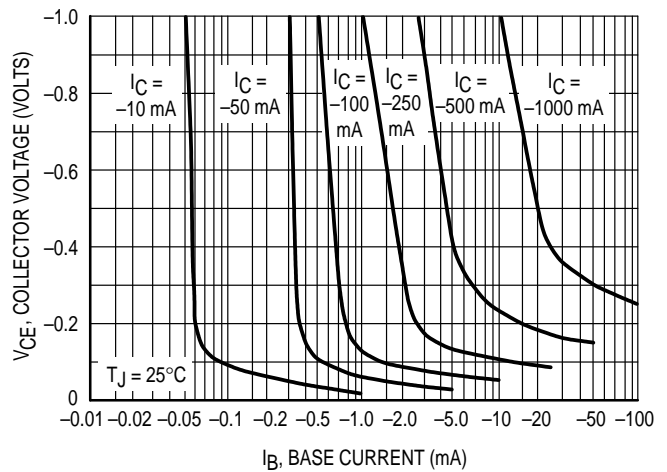


Figure 2. Collector Saturation Region

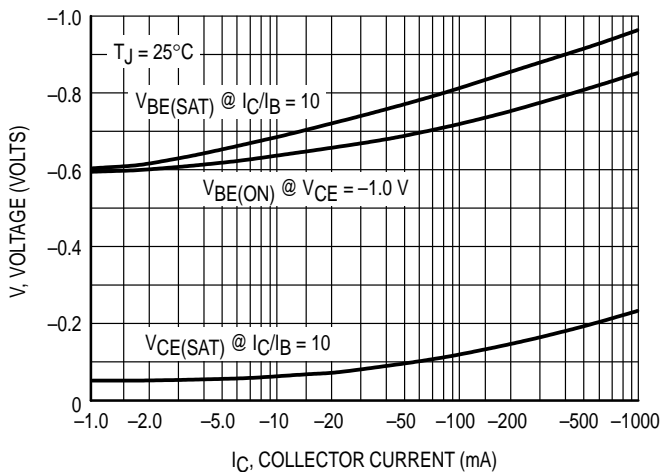


Figure 3. "ON" Voltages

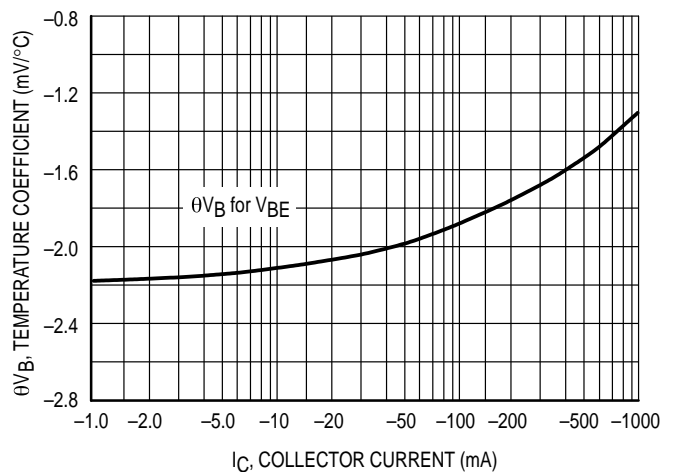


Figure 4. Temperature Coefficient

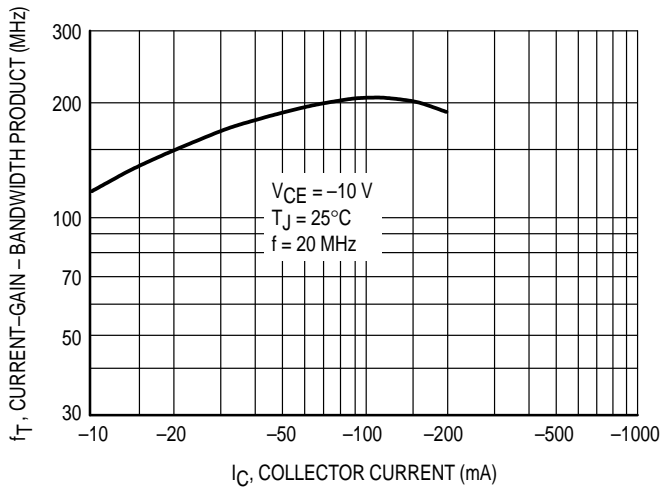


Figure 5. Current Gain — Bandwidth Product

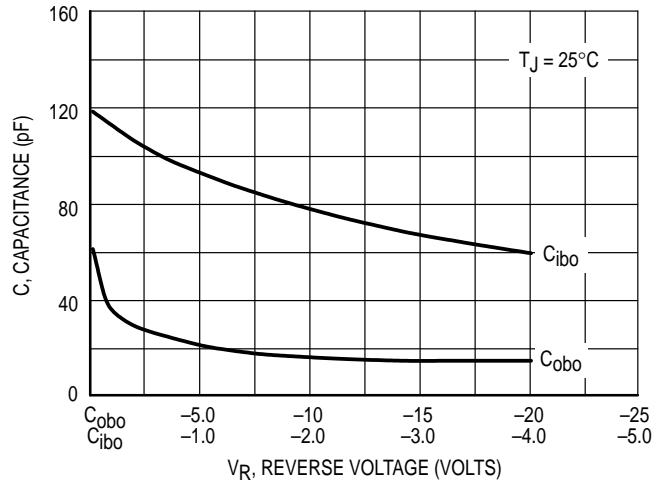


Figure 6. Capacitance

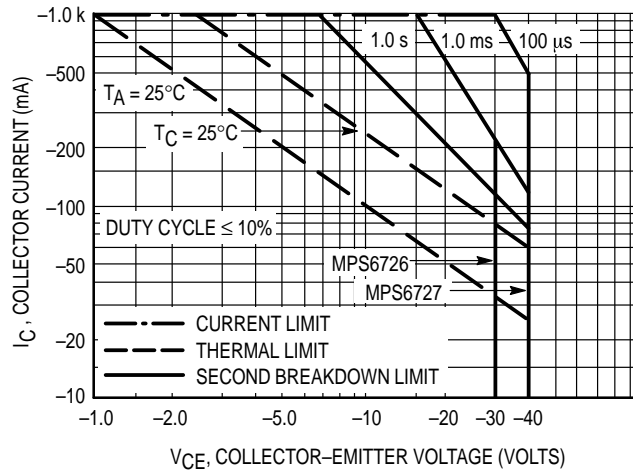
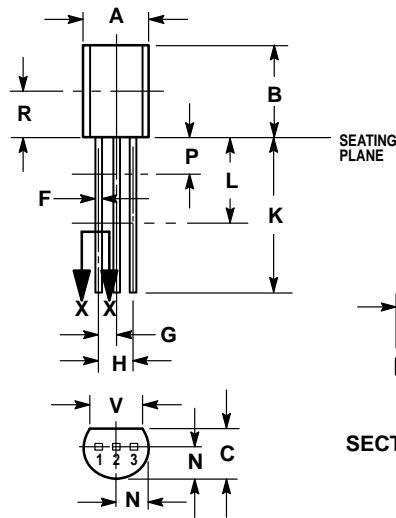


Figure 7. Active Region — Safe Operating Area

PACKAGE DIMENSIONS



SECTION X-X

CASE 029-05  
(TO-226AE)  
ISSUE AD


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.44	5.21
B	0.290	0.310	7.37	7.87
C	0.125	0.165	3.18	4.19
D	0.018	0.022	0.46	0.56
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500	—	12.70	—
L	0.250	—	6.35	—
N	0.080	0.105	2.04	2.66
P	—	0.100	—	2.54
R	0.135	—	3.43	—
V	0.135	—	3.43	—

STYLE 1:

- PIN 1. EMITTER
2. BASE
3. COLLECTOR

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